

Notice of References Cited	Application/Control No. 10/770,083	Applicant(s)/Patent Under Reexamination SHARMA ET AL.	
	Examiner Thanh V. Pham	Art Unit 2823	Page 1 of 1

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	B	US-5,705,432	01-1998	Lee et al.	438/606
	C	US-5,658,469	08-1997	Jennison, Michael J.	428/611
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NON-PATENT DOCUMENTS

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	U	"Schottky Barrier Diod Process", IBM Technical Disclosure Bulletin, Oct. 1979, US, Vol. 22, pp1872-1873.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.